

Abstract:

The invention relates to a method for actuating a semiconductor power switch, by which means the resistance of the switching path (E-A) of the semiconductor power switch is controlled by a control voltage (Vst) in such a way that the chip temperature (Tlist, T2ist) of the power switch (S1, S2) does not exceed a pre-determined nominal value (Tsoll). When the nominal temperature (Tsoll) is reached, the resistance of the switching path (E-A) is increased. The invention also relates to a device for carrying out said method, said device using a transfer gate (TG) controlled by a charge pump (LP) as a semiconductor power switch. Commercially available transistors comprising integrated temperature sensors are used in the transfer gate (TG) as semiconductor power switchs.

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